

ABSTRACT OF THE DISCLOSURE

An integrated circuit for use at a reduced Vdd voltage. An integrated circuit is designed and implemented such that it is usable at a voltage less than an industry standard Vdd voltage. The integrated circuit may include a voltage filter that may either be implemented on the integrated circuit or external to the integrated circuit. The voltage filter receives an industry standard IC voltage and produces a filtered voltage that is that some value below the industry standard IC voltage. The voltage filter also removes noise from the industry standard IC voltage. The integrated circuit includes signal processing circuitry that is designed and implemented to improve signal quality and to operate at the filtered voltage.

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WORKMAN NYDEGGER
A PROFESSIONAL CORPORATION
ATTORNEYS AT LAW
1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111